

Electronic Acknowledgement Receipt

EFS ID:	6049617
Application Number:	10549368
International Application Number:	
Confirmation Number:	2307
Title of Invention:	Pipelined instruction processor with data bypassing and disabling circuit
First Named Inventor/Applicant Name:	Balakrishnan Srinivasan
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Attorney Docket Number:	NL 030268
Receipt Date:	10-SEP-2009
Filing Date:	14-SEP-2005
Time Stamp:	16:54:45
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$810
RAM confirmation Number	3418
Deposit Account	141270
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Request for Continued Examination (RCE)	NL030268US1_RCE.pdf	697435 34f102b9a5ffee1e11e5e57e39a3d827aaa0792a	no	3
Warnings:					
Information:					
2		NL030268-US1_AMD_2009Sep10.pdf	595863 38a6ef93ecd4d227361cabca1f7b8d04db521bfd	yes	17
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment Submitted/Entered with Filing of CPA/RCE		1	1	
	Claims		2	9	
	Applicant Arguments/Remarks Made in an Amendment		10	17	
Warnings:					
Information:					
3	Fee Worksheet (PTO-875)	fee-info.pdf	30518 6a42b80b1de034285bb6427031620036d57e8ab7	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			1323816		
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